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PATENT  
Attorney Docket No.: SONY-14500

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Bruce A. Fairman

Serial No.: 09/708,114

Filed: November 6, 2000

For: **PROGRAMMABLE FIRST-IN  
FIRST-OUT (FIFO) MEMORY  
BUFFER FOR CONCURRENT  
DATA STREAM HANDLING**

) Group Art Unit: 2184

) Examiner:

) **INFORMATION DISCLOSURE**  
) **STATEMENT**) 260 Sheridan Avenue, Suite 420  
) Palo Alto, California 94306  
) (650)833-0160**RECEIVED**

SEP 30 2003

Technology Center 2100

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

The citations listed below, copies attached, may be material to the examination of the above-identified application, and are therefore submitted in compliance with the duty of disclosure defined in 37 C.F.R. §§ 1.56 and 1.97. The Examiner is requested to make these citations of official record in this application.

Applicant has become aware of the following printed publication which may be material to the examination of this application:

- U.S. Patent No. 3,836,722;
- U.S. Patent No. 3,906,484;
- U.S. Patent No. 4,218,756;
- U.S. Patent No. 4,379,294;
- U.S. Patent No. 4,395,710;
- U.S. Patent No. 4,409,656;
- U.S. Patent No. 4,493,021;
- U.S. Patent No. 4,633,392;
- U.S. Patent No. 4,641,238;
- U.S. Patent No. 4,641,307;

- 1 -

## CERTIFICATE OF MAILING (37 CFR § 1.8(a))

I hereby certify that this paper (along with any referred to as being attached or enclosed) is being deposited with the U.S. Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to the: Assistant Commissioner for Patents, Washington D.C. 20231

HAVERSTOCK &amp; OWENS LLP.

Date: 8/21/99 By: *John D. Haverstock*

- U.S. Patent No. 4,739,232;
- U.S. Patent No. 4,897,783;
- U.S. Patent No. 4,972,470;
- U.S. Patent No. 4,998,245;
- U.S. Patent No. 5,008,879;
- U.S. Patent No. 5,117,070;
- U.S. Patent No. 5,191,418;
- U.S. Patent No. 5,276,684;
- U.S. Patent No. 5,325,510;
- U.S. Patent No. 5,343,469;
- U.S. Patent No. 5,359,713;
- U.S. Patent No. 5,361,261;
- U.S. Patent No. 5,369,773;
- U.S. Patent No. 5,400,340;
- U.S. Patent No. 5,402,419;
- U.S. Patent No. 5,412,698;
- U.S. Patent No. 5,420,573;
- U.S. Patent No. 5,444,709;
- U.S. Patent No. 5,465,402;
- U.S. Patent No. 5,487,153;
- U.S. Patent No. 5,493,570;
- U.S. Patent No. 5,497,466;
- U.S. Patent No. 5,499,344;
- U.S. Patent No. 5,500,946;
- U.S. Patent No. 5,506,846;
- U.S. Patent No. 5,509,126;
- U.S. Patent No. 5,519,701;
- U.S. Patent No. 5,524,213;
- U.S. Patent No. 5,526,353;
- U.S. Patent No. 5,533,018;
- U.S. Patent No. 5,535,208;
- U.S. Patent No. 5,537,408;
- U.S. Patent No. 5,544,324;

- U.S. Patent No. 5,546,389;
- U.S. Patent No. 5,546,553;
- U.S. Patent No. 5,548,587;
- U.S. Patent No. 5,550,802;
- U.S. Patent No. 5,559,796;
- U.S. Patent No. 5,559,967;
- U.S. Patent No. 5,566,174;
- U.S. Patent No. 5,576,980;
- U.S. Patent No. 5,579,278;
- U.S. Patent No. 5,586,264;
- U.S. Patent No. 5,594,732;
- U.S. Patent No. 5,594,734;
- U.S. Patent No. 5,598,545;
- U.S. Patent No. 5,602,853;
- U.S. Patent No. 5,603,058;
- U.S. Patent No. 5,615,382;
- U.S. Patent No. 5,617,419;
- U.S. Patent No. 5,619,646;
- U.S. Patent No. 5,632,016;
- U.S. Patent No. 5,640,286;
- U.S. Patent No. 5,640,392;
- U.S. Patent No. 5,640,592;
- U.S. Patent No. 5,646,941;
- U.S. Patent No. 5,647,057;
- U.S. Patent No. 5,652,584;
- U.S. Patent No. 5,655,138;
- U.S. Patent No. 5,659,780;
- U.S. Patent No. 5,661,848;
- U.S. Patent No. 5,664,124;
- U.S. Patent No. 5,668,948;
- U.S. Patent No. 5,684,954;
- U.S. Patent No. 5,687,174;
- U.S. Patent No. 5,687,316;

- U.S. Patent No. 5,689,244;
- U.S. Patent No. 5,689,727;
- U.S. Patent No. 5,692,211;
- U.S. Patent No. 5,694,555;
- U.S. Patent No. 5,696,924;
- U.S. Patent No. 5,704,052;
- U.S. Patent No. 5,706,439;
- U.S. Patent No. 5,708,779;
- U.S. Patent No. 5,710,773;
- U.S. Patent No. 5,726,821;
- U.S. Patent No. 5,752,076;
- U.S. Patent No. 5,758,075;
- U.S. Patent No. 5,761,430;
- U.S. Patent No. 5,761,457;
- U.S. Patent No. 5,774,683;
- U.S. Patent No. 5,781,599;
- U.S. Patent No. 5,787,256;
- U.S. Patent No. 5,787,298;
- U.S. Patent No. 5,793,953;
- U.S. Patent No. 5,799,041;
- U.S. Patent No. 5,812,883;
- U.S. Patent No. 5,815,678;
- U.S. Patent No. 5,828,416;
- U.S. Patent No. 5,828,903;
- U.S. Patent No. 5,832,245;
- U.S. Patent No. 5,835,726;
- U.S. Patent No. 5,835,793;
- U.S. Patent No. 5,848,253;
- U.S. Patent No. 5,872,983;
- U.S. Patent No. 5,875,312;
- U.S. Patent No. 5,884,103;
- U.S. Patent No. 5,887,145;
- U.S. Patent No. 5,938,752;

- U.S. Patent No. 5,946,298;
- U.S. Patent No. 5,970,236;
- U.S. Patent No. 5,987,126;
- U.S. Patent No. 5,991,520;
- U.S. Patent No. 6,085,270;
- European Publication No. EP 0 428 111 A2;
- European Publication No. EP 0 499 394 A1;
- European Publication No. EP 0 588 046 A1;
- European Publication No. EP 0 696 853 A2;
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- "Asynchronous Transfer Mode" Julia L. Heeter, December 12, 1995;
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- "Fiber Channel (FCS)/ATM interworking: A design solution" A. Anzaloni, M. De Sanctis, F. Avaltroni, G. Rulli, L. Proietti and G. Lombardi, Ericsson Fatme R&D Division, November 1993, pp. 1127-1133;
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- "IEEE 1394-1995 Triple Cable Transceiver/ Arbiter," Texas Instruments TSB21LV03, Product Preview, Revision 0.99, March 19, 1996;
- "The IEEE-1394 High Speed Serial Bus." R.H.J. Bloks, Phillips Journal of Research, Vol. 50, pages 209-216, July 1996;
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- "IEEE 1394, The Cable Connection To Complete The Digital Revolution," Daniel Moore; and
- "P1394a Draft Standard for High Performance Serial Bus (Supplement)" P1394a Draft 2.0, March 15, 1998, pg. 1-166.

This Supplemental Information Disclosure Statement under 37 C.F.R. §§ 1.56 and 1.97 is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that anyone or more of these citations constitutes prior art.

Respectfully submitted,  
HAVERSTOCK & OWENS LLP

Dated: August 24, 2001

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